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L4 not l1

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L5 L4 not l1

35

L5

L4 L3 and (output gate?)

35

L4

L3 L2 and (input gate?)

156

L3

L2 gate? and channel? and model\$3 and software

5223

L2

L1 gate? and channel? and ((software adj unit?) or (software ensemble?))
 and model\$3

13

L1

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1 Associative and Parallel Processors

Kenneth J. Thurber, Leon D. Wald

December 1975 ACM Computing Surveys (CSUR), Volume 7 Issue 4

Full text available: pdf(2.62 MB) Additional Information: full citation, references, citings, index terms

2 An experience in the formal verification of industrial software

M. G. Staskauskas

December 1996 Communications of the ACM

Full text available: pdf(158.61 KB) Additional Information: full citation, references, citings, index terms

3 S-connect: from networks of workstations to supercomputer performance

Andreas G. Nowatzky, Michael C. Browne, Edmund J. Kelly, Michael Parkin

May 1995 ACM SIGARCH Computer Architecture News , Proceedings of the 22nd annual Computer architecture, Volume 23 Issue 2

Full text available: pdf(1.38 MB) Additional Information: full citation, abstract, references, citin

S-Connect is a new high speed, scalable interconnect system that has been developed for workstations to efficiently share computing resources. It uses off-the-shelf CMOS fiber-optic systems at speeds greater than 1 Gbit/sec and can realize bisection bandwidth MPP systems while being >10x more cost-effective. S-Connect systems do not rather are composed of adaptive, topology independent ...

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